

# HCMOS OUTPUT SMD (11.4x9.6 mm) TCXO/VCTCXO IN LEADLESS PACKAGE - TCLC Series

#### **FEATURES**

- RoHS Compliant (Pb-Free), HCMOS/TTL Compatible Square Wave Output
- Voltage Control Option for Electric Frequency Adjustments
- Low Phase Noise, Trimmerless, Reflow Soldering
- SMD Miniature Size, Industry de factor Standard Footprint

### **SPECIFICATIONS**

**Frequency Range** 1.250 MHz to 40.000 MHz

Supply Voltage (Vcc)  $A = 5.0 \text{ VDC} \pm 5\%$ ;  $B = 3.3 \text{ VDC} \pm 5\%$ 

30 mA Maximum Input Current **Storage Temperature** -55°C to 125°C

**Controllable Frequency Option** 

V = Voltage control option: ±8 ppm Minimum

Control Voltage (Vc) 2.5±2.0 VDC for Vcc = 5 VDC; 1.65±1.5 VDC for Vcc = 3.3 VDC

Setability of Vc at Fnom, 25°C 2.5±0.5 VDC for 5.0V part; 1.65±0.4 VDC for 3.3V part

Frequency Stability vs Temp.

 $010 = \pm 1 \text{ ppm}$ ;  $020 = \pm 2 \text{ ppm}$ ;  $025 = \pm 2.5 \text{ ppm}$ Temperature Range  $A = 0^{\circ}C$  to  $70^{\circ}C$ ;  $B = -40^{\circ}C$  to  $85^{\circ}C$ ;  $H = -30^{\circ}C$  to  $75^{\circ}C$ 

Standard Stability  $025H = \pm 2.5 \text{ ppm} / -30^{\circ}\text{C} \text{ to } 75^{\circ}\text{C}$ Frequency Stability vs Vcc ±0.2 ppm Maximum / Vcc ± 10% Frequency Stability vs Load ±0.2 ppm Maximum / 15 pF ±10%

Aging ±1 ppm Maximum per year @25°C

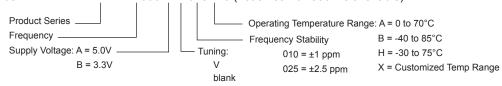
**Output Load** CMOS 15 pF

Logic "1" / Logic "0" Level 0.9Vcc Minimum / 0.1Vcc Maximum

Rise/Fall Time (Tr/Tf) 10 ns Maximum Start-up time 10 ms Maximum

**Duty Cycle** 0 = Non-tristate 60/40% Phase Noise (typ) -140 dBc/Hz at 10KHz

#### TCLC-27M000-B V 025 H 0 (not all combination is available) **Creating a Part Number**



## **OUTLINE DRAWING**

