

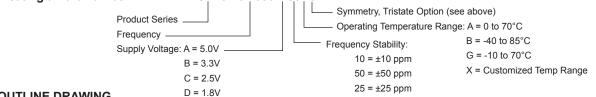
HCMOS/TTL CRYSTAL OSCILLATOR IN 7x5 mm QFN PLASTIC PACKAGE - XOP75 Series

FEATURES

- RoHS Compliant (Pb-Free), Industry Standard Pin-out Spacing
- Very Low Phase Jitter with Fundamental Crystal Design •
- Tri-state Enable/Disable Standard; 5V, 3.3V, 2.5V or 1.8V Option •
- Plastic Molded QFN Package (7x5x1.25 mm); MSL = 1 •

SPECIFICATIONS

Frequency Range	0.5 MHz to 54.0MHz
Input Voltage (Vcc) Input Current Storage Temperature	A = +5VDC \pm 10%; B = +3.3VDC \pm 10%; C = 2.5VDC \pm 10%; D = 1.8VDC \pm 10% 40 mA Maximum, depending on frequency and output load -55°C to 125°C
Overall Frequency Stability Temperature Range Standard Stability	50 = ±50 ppm; 30 = ±30 ppm; 25 = ±25 ppm; 10 = ±10 ppm A = 0°C to 70°C; B = -40°C to 85°C; D = -20°C to 70°C; G = -10°C to 70°C 50B = ±50 ppm / -40°C to 85°C
Electric Option (Symmetry)	1 = Tristate 60/40%; 3 = Tristate 55/45%; 5 = Tristate 52.5/47.5%
Output Load Logic "1" / Logic "0" Level Rise/Fall Time (Tr/Tf) Start-up time Phase Jitter (RMS, 1 Sigma)	HCMOS: Drive up to 50 pF load; TTL: Drive up to 10 TTL gates 0.9Vcc Minimum / 0.1Vcc Maximum 10 ns Maximum, depending on frequency and output load 10 ms Maximum 1 ps Maximum for fj > 1kHz; 0.3 ps Typical for fj = 12KHz to 20MHz
Tristate Function	Input (Pin 1) High (> 0.7Vcc, or 2.2V if Vcc=5V) or open: Output (Pin 3) active Input (Pin 1) Low (< 0.3Vcc, or 0.8V if Vcc=5V): Output disabled in high impedance
Output Disabled Time Standby Current Output Enable Time	100 ns Maximum 100 ns Maximum
Creating a Part Number	XOP75-25M000-B50B3



OUTLINE DRAWING

